

## Logic Powered Serial EEPROMs

Embedded applications increasingly want more integration and power, in less space for less cost. Using low power Serial EEPROMs (SEE) for application firmware, lookup tables, and microcode coupled with small footprints makes for permanent storage at respectable savings. One additional method of saving on the power budget is selectively powering off components when not needed, a basic for embedded power management. The low-power SEEs offered by Microchip Technology Inc., offer an additional benefit, powering the SEE from a microcontroller port. This allows the host controller to not only manipulate the Serial EEPROM Reads and Write, but also the periods when it is powered off or on. Satellite communications use this technique to save power and total dose accumulation. We call this technique POWER PORT™. The microcontroller port must have sufficient Ioh (source current) to sustain the voltage and current for all memory functions, READ, ERASE, and WRITE. Obviously, not all memory or peripheral devices could be powered thusly, but Microchip's SEE devices will function in this environment.

The microcontroller, using its internal software and hardware decision functions, determines when it needs to communicate with the memory device, then acts accordingly. Any standard wake-up sequence will accomplish this task. The wake-up code needs only power up the memory and wait for the power to become stable before doing a read or write by driving the POWER PORT high. Then all serial communication executes normally. The SEEs are powered off for additional power savings and the data or code is utilized from RAM. Obviously, the port output must be allowed to settle, but normal operation of the output structures would guarantee that this would be met. The I/O port Tpd for the Microchip PIC16C5X, is specified at 40ns maximum.

The 24LCXX and 93LCXX CMOS SEE series parts from Microchip were designed to achieve low current consumption across all ranges of operation.

The four primary Icc parameters for these products are:

<u>Parameter</u>	<u>Conditions</u>
Icc STANDBY	Not in an active operation while Vcc is supplied.
Icc READ	The part is in a READ operation.

**Icc PEAK WRITE**      The BYTE / PAGE WRITE and ERASE operations have self timed cycles of 10 ms. A typical of 4 ms is the actual time of the operation. This is the amount of time when the Icc requires the most current (PEAK WRITE). The part is drawing STANDBY Icc during the remaining 6ms of the cycle.

**Icc AVG WRITE**      The avg of the PEAK WRITE Icc and STANDBY Icc during the self-timed 10ms write cycle.

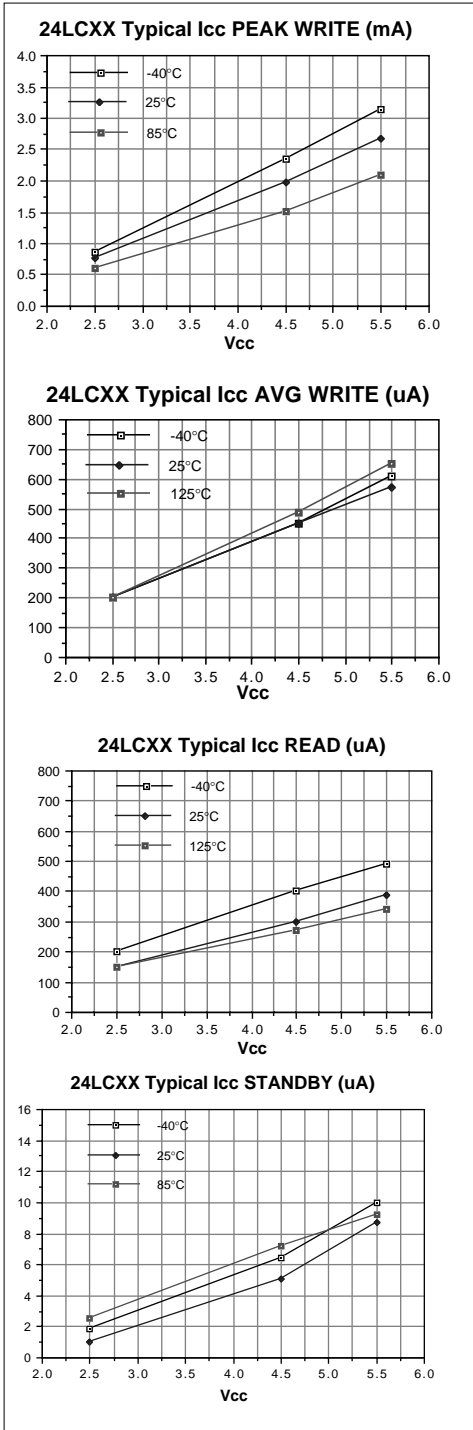
The attached characteristic curves (Figures A and B) indicate that Icc PEAK WRITE current consumes the most current. The worst case condition is at 6.0V and -40°C. The 24LCXX series parts draw a typical 3.2 mA and the 93LCXX series parts draw a typical of 2.0 mA. These low Icc characteristics offer a unique current saving benefit for battery applications. Figure C and D illustrate the sink and source current capabilities of the PIC16C5X family of microcontrollers. It is clear from these characterization curves that the microcontroller can deliver sufficient current across all temperature ranges to power a SEE using the POWER PORT technique.

Figure E shows the connection scheme for the Microchip PIC16C54. It should be noted that not all versions of competitive microcontrollers are capable of powering a device in this manner and the specific data sheets for the microcontroller being considered must be consulted for maximum source current. The microcontroller port must be capable of sourcing sufficient current for the duration of the write cycle or 10ms, worse case. The peak write requirement for the 24LCXX product family is 3.2 mA at 5.5 Vdc (-40°C).

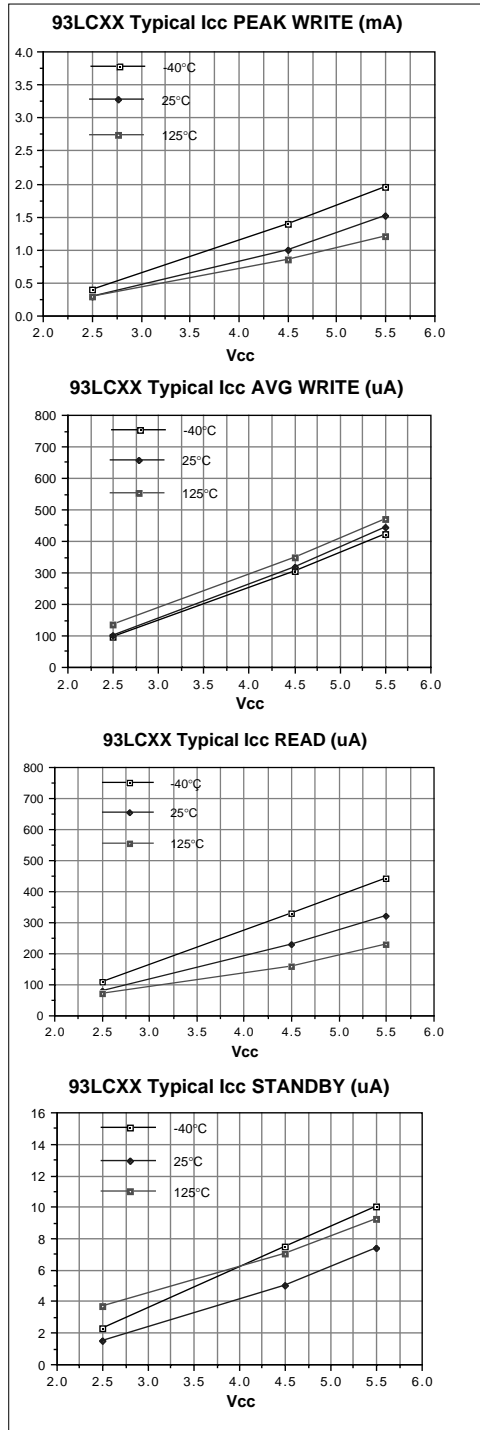
Listing A demonstrates the appropriate code sequences when using the PIC16C54 microcontroller. The sequences included are power control, start bit, stop bit, send and receive bit, Tx and Rx, and a general addressing routine.

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**FIGURE A - TYPICAL  $I_{cc}$  FOR 24LCXX**



**FIGURE B - TYPICAL  $I_{cc}$  FOR 93CXX**



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FIGURE C - PIC16C5X I<sub>OL</sub> AT 5V

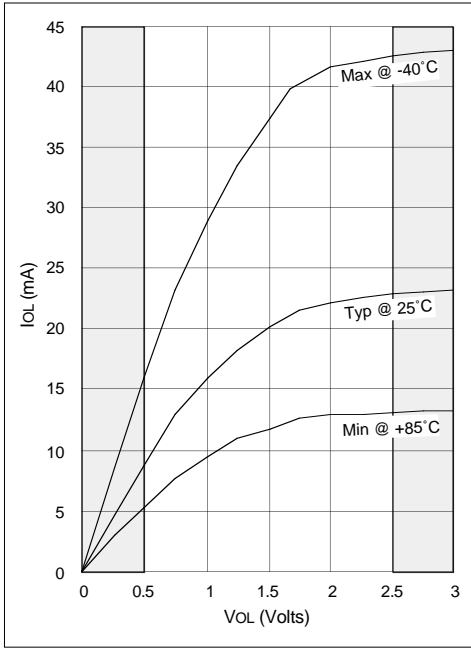


FIGURE D - PIC16C5X I<sub>OH</sub> AT 5V

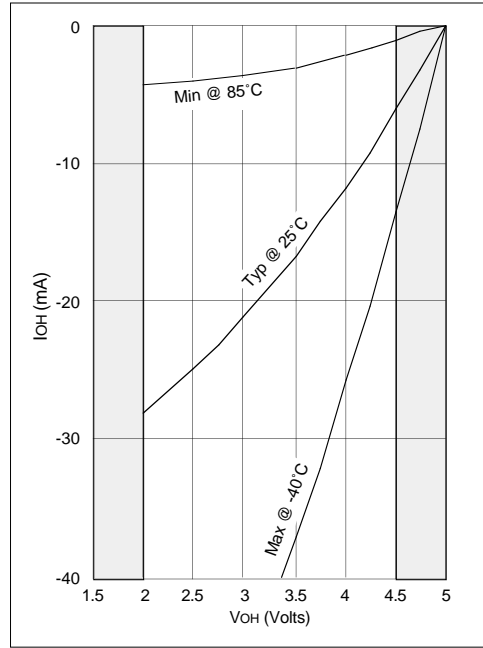
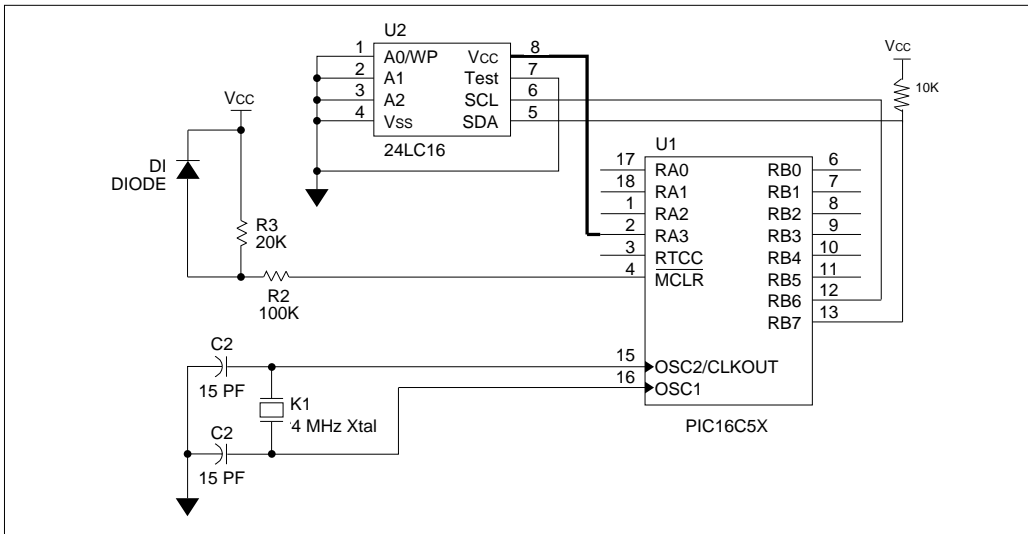


FIGURE E - 24LC16/PIC16C5X INTERFACE SCHEMATIC



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The primary benefits of this application are:

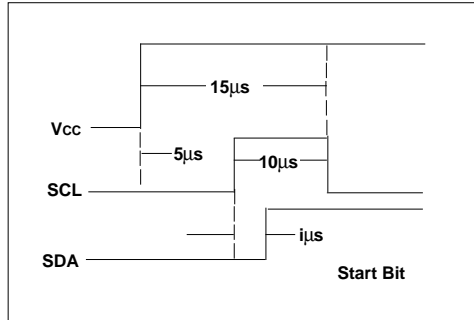
- The SEE is completely powered down to save power when the SEE is not executing an operation. This will directly effect the total system power consumption. This means that the SEE is in a total quiescent state and even the standby current savings are realized, greatly increasing usable battery life, and consequently allowing for a more sophisticated design on the same power budget.
- The very fast 5  $\mu$ s power-up time minimizes power-up delay.
- Since the serial operation is gated by a stable microcontroller V<sub>OH</sub>, risk of data being corrupted by a glitch is minimized. This, in effect, is a regulated V<sub>CC</sub> supply and provides a reliable power source to ensure data integrity.

Several cautions need to be noted:

1. *Gang powering multiple devices must not exceed the I/O port I<sub>OH</sub> or capacitive load specifications.*
2. *The total power requirements vs. power budget must be considered, including the extra drain on the microcontroller.*
3. *The microcontroller I<sub>CC</sub> max must not be exceeded.*
4. *Normal decoupling methods must be employed.*
5. *The microcontroller I<sub>OH</sub> for the port in use must not be exceeded.*

Figure F shows a typical power on to start bit sequence. Notice that the device is available to receive a clock at 5  $\mu$ s after V<sub>CC</sub> has become stable.

FIGURE F



Many applications, especially remote or handheld data acquisition applications, where power consumption is at a premium or battery life is critical can use the POWER PORT technique with the PIC16/17 microcontrollers and possibly other microcontrollers. Remote metering applications where the microcontroller must wake up and report previously stored data or periodically sample inputs, such as gas, electrical, or water monitoring systems are good examples where POWER PORT would be beneficial. Underground monitoring equipment for fuel storage and environmental monitoring systems are also suitable applications.

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## LISTING A

```

LIST P=16C54
;
;
;   Sample test program to power up serial EEPROM
;   using PIC16/17 port A, then write one byte and read same byte, then repeat forever.
;
;*****
port_a equ    5h    ; port 5 used for device
                ; address select
port_b equ    6h    ; port 6 used for data and
                ; clock lines
eeprom equ    0ah   ; bit buffer
addr  equ    0ch   ; address register
datai equ    0dh   ; stored data input reg.
datao equ    0eh   ; stored data output reg.
slave equ    0fh   ; device address
                ; (1010xxx0)
txbuf  equ    10h  ; tx buffer
count  equ    11h  ; bit counter
bcount equ    12h  ; byte counter
rxbuf  equ    13h  ; receive buffer
loops  equ    15h  ; delay loop counter
loops2 equ    16h  ; delay loop counter 2
;
;   Bit Assignments
;
di      equ    7    ; eeprom input
do      equ    6    ; eeprom output
sdata  equ    7    ; data line (port_b)
sclk   equ    6    ; clock line (port_b)
vcc    equ    3    ; vcc for dut (port_a)
;
begin   goto    PWRUP
        org     000h
        goto    PWRUP
;
;*****
;   DELAY ROUTINE
;   this routine takes the value in loops and loops that many times. Every
;   increase in 'loops' yields approx 1 more millisecond.
;   i.e., if 'loops' is 10 then the wait period is approx 10 milliseconds.
;
;-----
WAIT
;
top2    movlw   .110
        movwf  loops2
top     nop     ; sit and wait
        nop
        nop
        nop
        nop
        nop
        decfsz loops2 ; inner loop done?
        goto  top   ; no, go again
        decfsz loops ; outer loop done?
        goto  top2  ; no, go again
        retlw  0    ; yes, return from sub
;
;*****

```

# Logic Powered Serial EEPROMs

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---

```
;      Start Bit Subroutine
;      this routine generates a start bit
;-----
;
BSTART
    movlw  b'00111111'
    tris   port_b      ; port b for output
    bsf   port_b,sdata ; set clock high
    nop
    nop
    bsf   port_b,sclk  ; set clock high
    nop
    nop
    nop
    nop
    nop
    nop
    nop
    nop
    nop
    bcf   port_b,sdata ; data line goes low during high clock for start bit
    nop
    nop
    nop
    nop
    nop
    bcf   port_b,sclk  ; start clock train
    nop
    nop
    nop
    retlw  0
    ;
    ;      End of Subroutine
;*****
;
;      Stop Bit Subroutine
;      this routine generates a stop bit
;-----
;
BSTOP
    movlw  b'00111111' ;
    tris   port_b      ; set data/clock lines as outputs
    bcf   port_b,sdata ; make sure data line is low
    nop
    nop
    nop
    nop
    nop
    bsf   port_b,sclk  ; set clock high
    nop
    nop
    nop
    nop
    nop
    bsf   port_b,sdata ; data goes high while clock high
                        ; for stop bit
    nop
    nop
    nop
    nop
    bcf   port_b,sclk  ; set clock low again
    nop
    nop
    nop
    retlw  0
;
;      End of Subroutine
;*****
```

# Logic Powered Serial EEPROMs

```
; Serial data send 1 bit from PIC16/17 to dut
;-----
BITOUT
    movlw    b'00111111'    ; set data,clock as outputs
    tris     port_b
    btfss   eeprom,do
    goto    BIT0
    bsf     port_b,sdata    ; output bit 0
    goto    CLK1           ; data line clocked low by device
;
BIT0
    bcf     port_b,sdata    ; output bit 0
CLK1
    nop
    nop
    bsf     port_b,sclk     ; set clock line high
BIT2
    nop
    nop
    nop
    nop
    bcf     port_b,sclk     ; return clock line low
    retlw   0
;
; End of Subroutine
;
;-----
;*****
; Bit in routine
; this routine gets a bit of data from the part
; into the 'eeprom' register, bit 'di'
;-----
BITIN
    movlw    b'10111111'    ; make sdata an input line
    tris     port_b
    bcf     eeprom,di       ; assume input bit low
    bsf     port_b,sclk     ; set clock line high
    nop
    nop                    ; just sit here a sec
    nop
    nop
    nop
    nop
    nop
    nop
    nop
    nop
    nop
    btfsc   port_b,sdata    ; read data line
    bsf     eeprom,di       ; set input bit if needed
    bcf     port_b,sclk     ; set clock line low
    retlw   0               ; hit the road
;
;*****
;
; Transmit Data Subroutine
;-----
TX
    movlw    .8
    movwf   count          ; set the #bits to 8
;
TXLP
    bcf     eeprom,do
    btfsc   txbuf,7
    bsf     eeprom,do      ; otherwise data bit =1
    call    BITOUT         ; serial data out
    rlf     txbuf          ; rotate txbuf left
    decfsz count          ; 8 bits done?
    goto    TXLP          ; no - go again
    call    BITIN         ; read ack bit
;
    retlw   0
; End of Subroutine
;-----
;*****
```

# Logic Powered Serial EEPROMs

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```
; Receive data Routine
; this routine gets a byte of data from the part into 'rxbuf'
;-----
RX
    movlw    .8           ; set # bits to 8
    movwf   count
    clrf    rxbuf        ; clear receive buffer
RXLP   rlf    rxbuf        ; rotate buffer left 1 bit
    bcf    rxbuf,0       ; assume bit is zero
    call   BITIN         ; read a bit
    btfsc  eeprom,di     ; input bit high?
    bsf    rxbuf,0       ; yes, set buffer bit high
    decfsz count        ; 8 bits done?
    goto   RXLP         ; no, do another
    bcf    eeprom,do     ; set ack bit = 0
    call   BITOUT        ; to finish transmission
    retlw  0
;
;*****
; Power up routine
; this routine blinks the lights
;-----
PWRUP
    movlw  b'00000001'
    tris  port_a        ; set RA0 as input, rest output
    bsf   port_a,vcc    ; turn on power to dut
    nop
    nop                ; wait for dut to power up
    nop
    nop
    nop
;
;*****
; Byte Write Routine
; this writes the data in "55h" to the first byte
; in the serial EEPROM.
;-----
;
;
WRBYTE
    ;
    movlw  b'10100000'  ; set slave address and write mode
    movwf  slave
    movlw  b'01010101'  ; set data to 55h
    movwf  data0
    ;
    clrf  addr          ; set address to 00h
    ;
    call  BSTART        ; generate start bit
    movf  slave,w       ; get slave address
    movwf txbuf         ; into transmit buffer
    call  TX            ; and send it
    movf  addr,w        ; get word address
    movwf txbuf         ; into transmit buffer
    call  TX            ; and send it
    movf  data0,w       ; move data
    movwf txbuf         ; to transmit buffer
    call  TX            ; and transmit it
    call  BSTOP         ; generate stop bit
    ;
    movlw .10
    movwf loops        ; set delay time to give
    call  WAIT          ; 10 ms wait after every byte
    ;
    ; now drop through and do the read
    ;
;*****
; READ (read routine)
; this routine reads the first address
; of the dut
```



# Logic Powered Serial EEPROMs

```
;  
READ  
  
    movlw  b'10100000' ; set slave address and write mode  
    movwf  slave  
  
    clrf   addr        ; set address to 00h  
  
    call  BSTART      ; generate start bit  
    nop  
    nop  
    movf  slave,w     ; get slave address  
    movwf txbuf       ; into transmit buffer  
    call  TX          ; and send it  
    movf  addr,w     ; get word address  
    movwf txbuf       ; into transmit buffer  
    call  TX          ; and send it  
    nop  
    nop  
    call  BSTART      ; generate start bit  
    nop  
    nop  
    movlw b'10100001' ; get slave address and read mode  
    movwf txbuf       ; into transmit buffer  
    call  TX          ; and transmit it  
    nop  
    call  RX          ; get 8 bits of data  
    bsf   eeprom,do   ; send high ack bit and then a  
    call  BITOUT      ; stop bit to end transmission from dut  
    call  BSTOP       ;  
    nop  
    nop  
    nop  
    bcf   port_a,vcc  ; turn power to dut off  
    movlw .100  
    movwf loops  
    call  WAIT        ; wait awhile  
    goto  PWRUP       ; go do the whole thing over again  
  
;  
END
```

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NOTES:

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