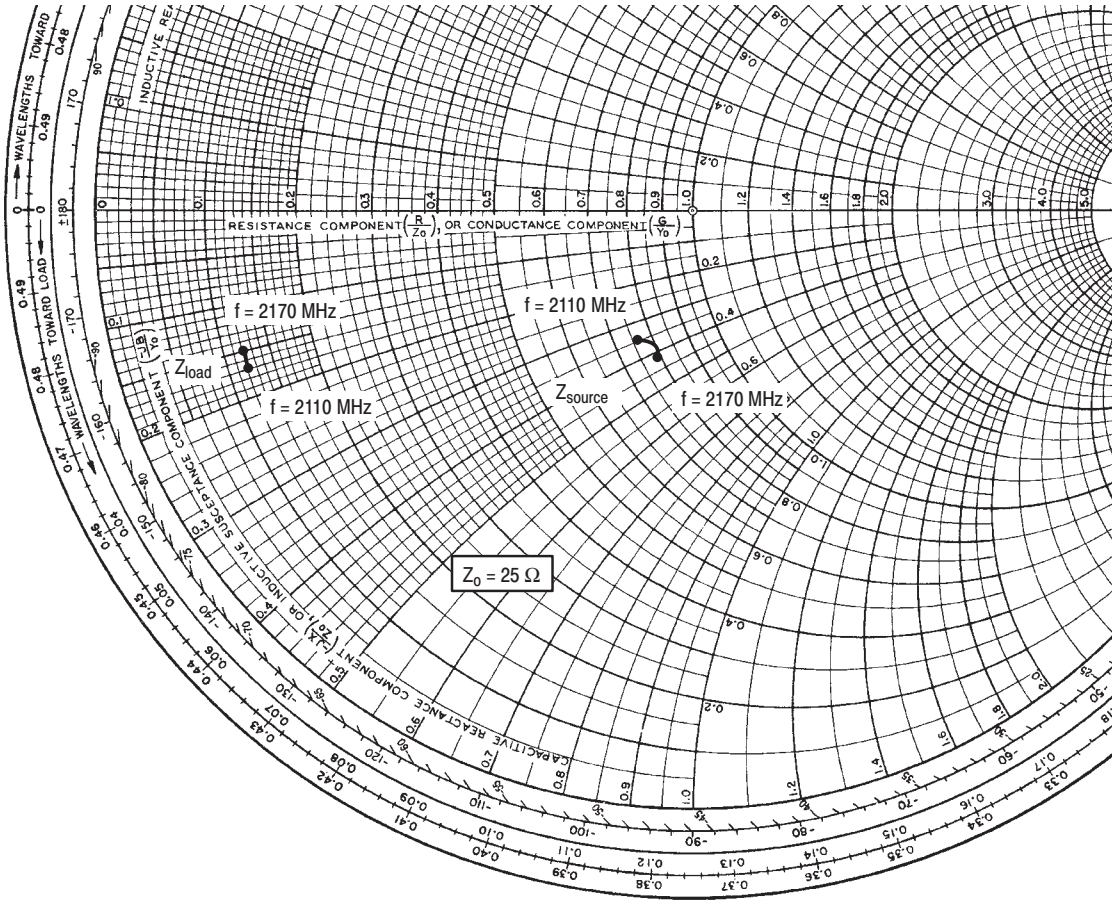


Freescale Semiconductor, Inc.



$V_{DD} = 28 \text{ Vdc}$, $I_{DQ} = 500 \text{ mA}$, $P_{out} = 10 \text{ W Avg.}$

f MHz	Z_{source} Ω	Z_{load} Ω
2110	$18.88 - j8.86$	$3.11 - j4.18$
2140	$19.80 - j9.93$	$3.09 - j3.87$
2170	$19.68 - j10.44$	$3.12 - j3.72$

Z_{source} = Test circuit impedance as measured from gate to ground.

Z_{load} = Test circuit impedance as measured from drain to ground.

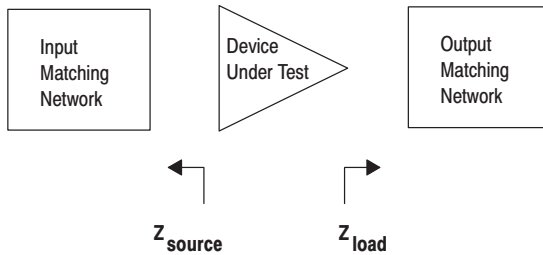


Figure 13. Series Equivalent Source and Load Impedance